



PARLE TILAK VIDYALAYA ASSOCIATION'S
MULUND COLLEGE OF COMMERCE (AUTONOMOUS)

(Affiliated to the UNIVERSITY OF MUMBAI)

MULUND VANIJYA MAHAVIDYALAYA MARG, MULUND (WEST), MUMBAI-400 080

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NAAC ACCREDITED GRADE "A"

(Cycle-I 2004, Cycle-II 2011, Cycle-III 2016)

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Date: 02/01/2026

Timetable
B.Sc. (Computer Science)
Continuous Internal Assessment – I (2025-26)

T.Y.B.Sc. COMPUTER SCIENCE SEMESTER VI		
DATE	TIME	COURSE
27-01-2026	07:30 – 08:10 a.m.	Data Science Fundamentals
	08:15 – 08:55 a.m.	Web Services & Cloud Computing
28-01-2026	07:30 – 08:10 a.m..	Digital Forensics
	08:15 – 08:35 a.m.	Introduction to Blockchain
29-01-2026	07:30 – 08:10 a.m.	Internet of Things
30-01-2026	07:30 – 09:00 a.m.	PLAT

S.Y.B.Sc. COMPUTER SCIENCE SEMESTER IV		
DATE	TIME	COURSE
27-01-2026	09:15 – 09:55 a.m.	Web Programming
	10:00 – 10:40 a.m.	Theory of Computation
28-01-2026	09:15 – 09:55 a.m.	Agile Scrum
	10:00 – 10:30 a.m.	Startup & Entrepreneurship Skills
29-01-2026	09:15 – 09:55 a.m.	Linear Algebra

F.Y.B.Sc. COMPUTER SCIENCE SEMESTER II		
DATE	TIME	COURSE
24-01-2026	07:30 – 09:30 a.m.	Practical Accounting – Batch 1
	09:45 – 11:45 a.m.	Practical Accounting – Batch 2
27-01-2026	11:00 – 11:40 a.m.	Computer Networks
	11:45 – 12:15 p.m.	English Technical Writing Skills
28-01-2026	11:00 – 11:40 a.m.	Advanced Python Programming
	11:45 – 12:15 p.m.	Principles of Accounting
29-01-2026	11:00 – 11:40 a.m.	Object Oriented Programming with C++
	11:45 – 12:10 p.m.	Advanced Calculus
	12:15 – 12:45 p.m.	Fundamentals of Indian Knowledge System
30-01-2026	09:30 – 11:00 a.m.	PLAT


Co-Ordinator


SCT Coordinator


Chairperson Examination Committee


Principal